

DEC 18 2006

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JIAWEI HUANG	(Depositor's name)
	(Signature)
DECEMBER 18, 2006	(Date)

12/19/2006 TTRAN2 00000103 500710 10823238

01 FC:2501 700.00 DA
 02 FC:1504 300.00 DA

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/823,238	04/12/2004	Chih-Feng Huang	JCLA12521	4073

TITLE OF INVENTION: PROCESS OF FABRICATING HIGH RESISTANCE CMOS RESISTOR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	YES	\$700	\$300	\$0	\$1000	01/12/2007

EXAMINER	ART UNIT	CLASS-SUBCLASS
LE, THAO P	2818	438-210000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

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2. For printing on the patent front page, list

(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

SYSTEM GENERAL CORP.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

TAIPEI HSIEN, TAIWAN

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Date 12/18/2006

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JIAWEI HUANG

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